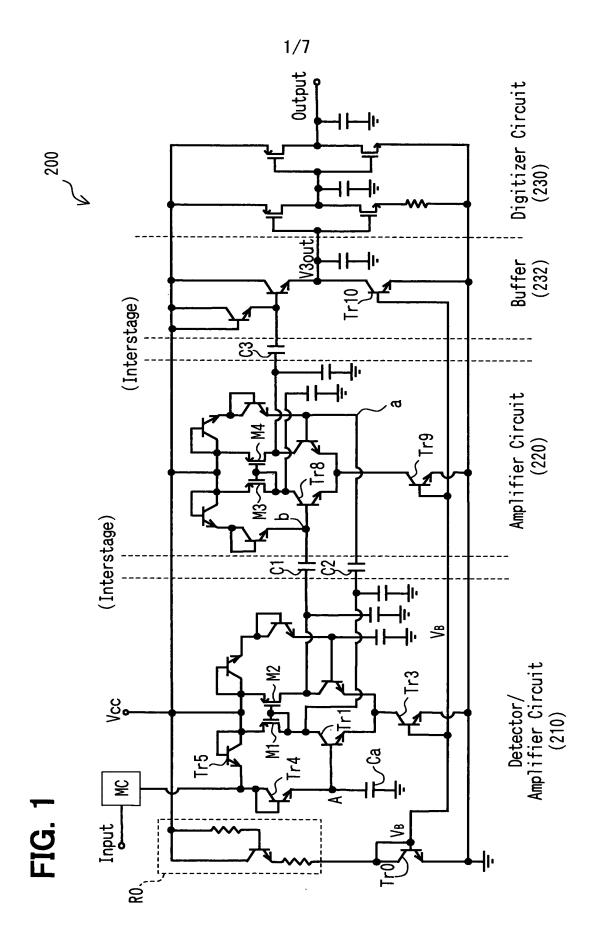
Inventor(s): Hisanori UDA et al. Atty. Ref.: 4041J-001077/US/NP



Title: ACTIVATION SIGNAL OUTPUT CIRCUIT

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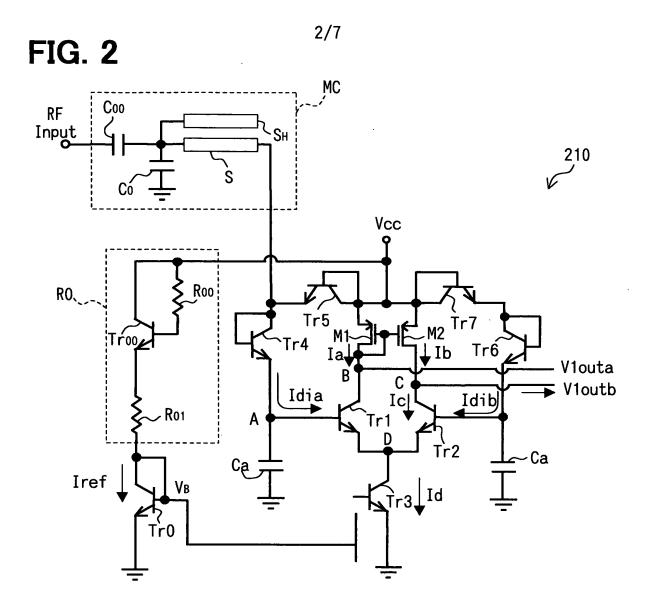
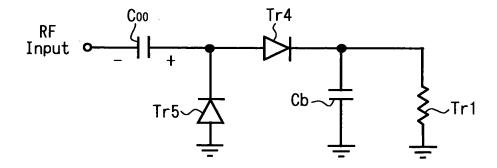


FIG. 3

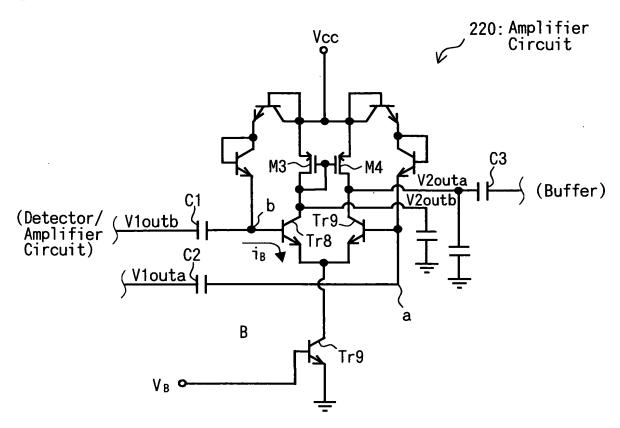


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FIG. 4

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FIG. 5A



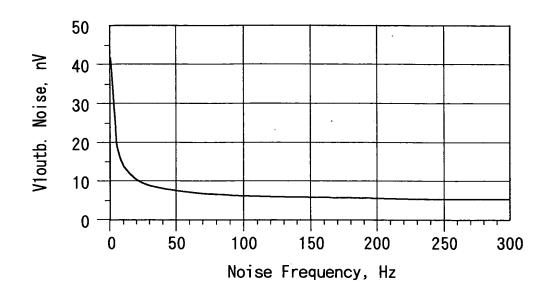
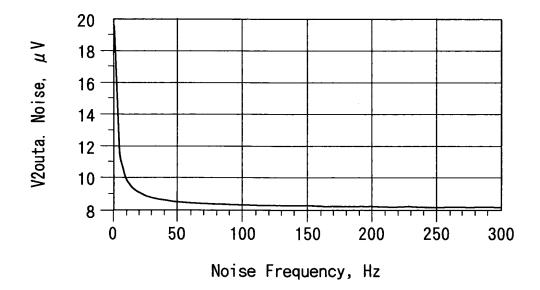


FIG. 5B



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FIG. 6A

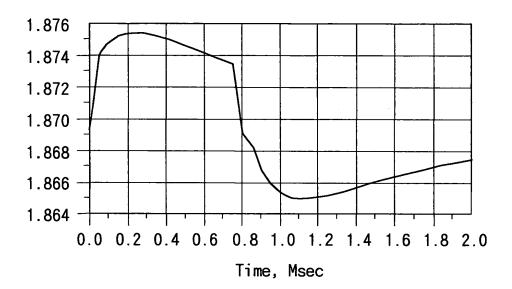
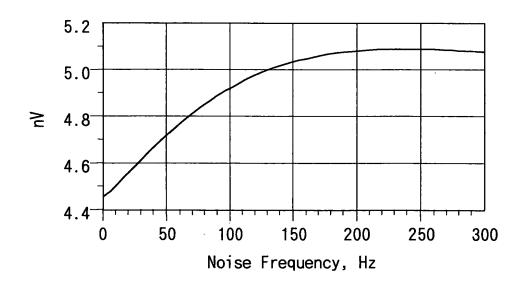


FIG. 6B



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FIG. 7A



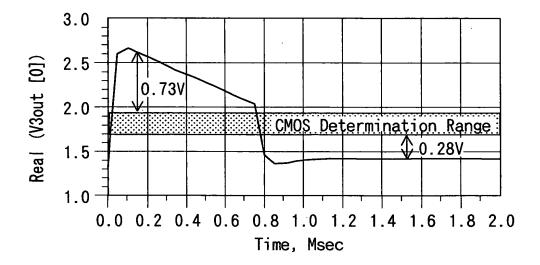
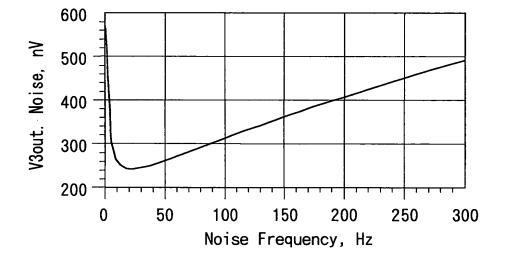


FIG. 7B



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FIG. 7C

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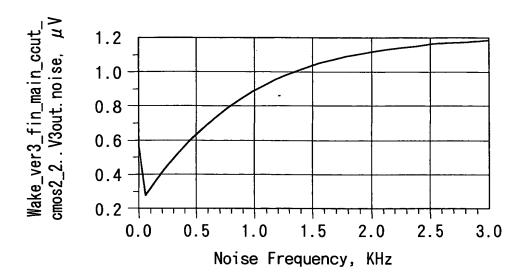


FIG. 8A

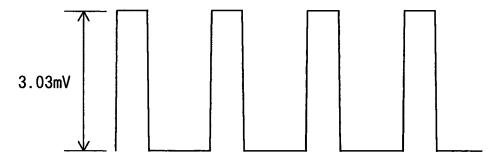


FIG. 8B

